

Fig. 2

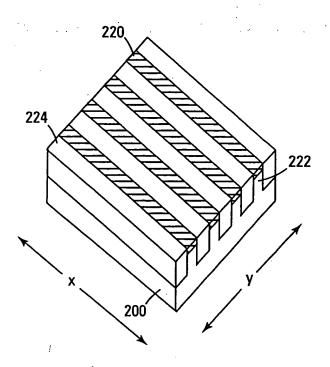


Fig. 4

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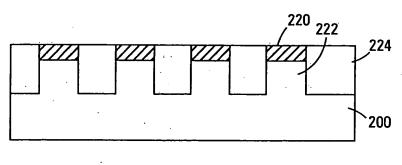
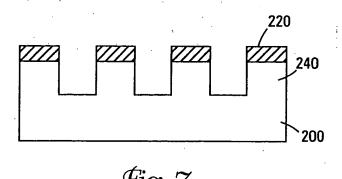


Fig. 5



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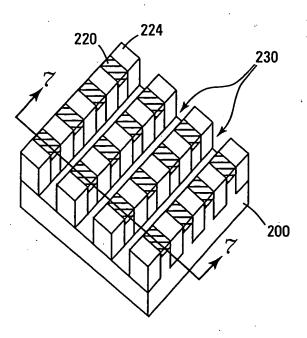
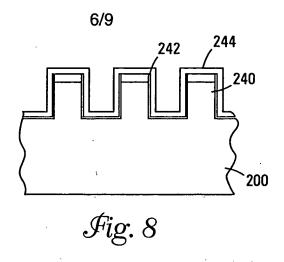
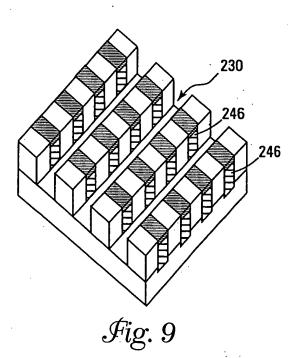


Fig. 6

Inventor: Paul Rudeck VERTICAL FLOATING GATE TRANSISTOR Atty Docket No. 400.178US02





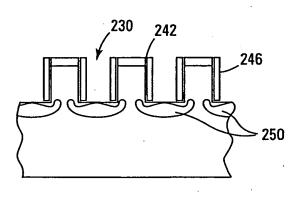
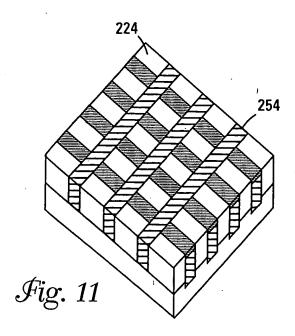


Fig. 10

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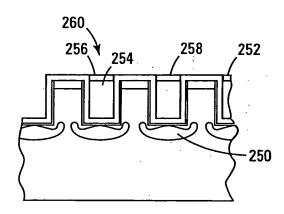
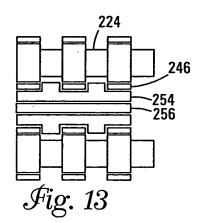
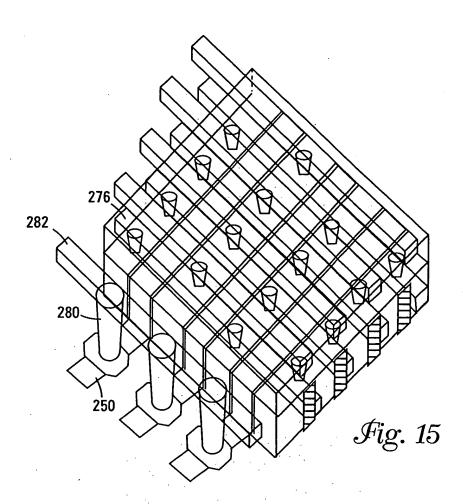


Fig. 12





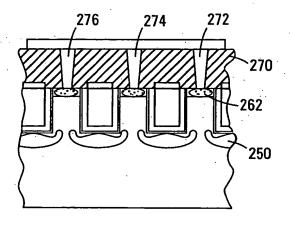


Fig. 14

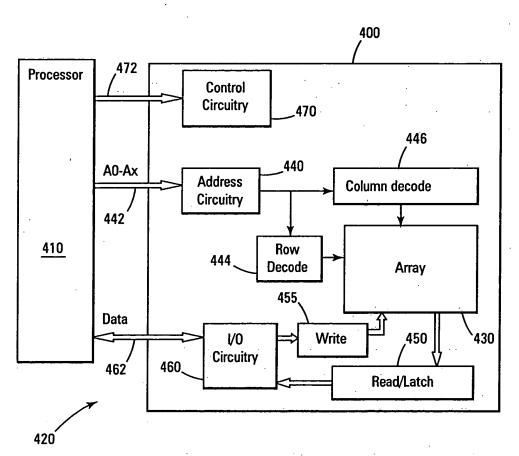


Fig. 16